NAME: G.PALLAVI

REG NO: 192111601

COURSE NAME: COMPUTER ARCHITECTURE FOR CONTROL SIGNAL GENERATION

COURSE CODE: CSA1290

# EXP 1:

LDA 2050

MOV B,A

LDA 2051

ADD B

STA 2052

HLT

# EXP 2:

LDA 2050

MOV B,A

LDA 2051

SUB B

STA 2052

HLT

# EXP 3:

# LHLD 2500

# XCHG

# LHLD 2502

# MOV A,E

# ADD L

# MOV L,A

# MOV A,D

# ADC H

# MOV H,A

# SHLD 2504

# HLT

# EXP 4:

LHLD 2500

XCHG

LHLD 2502

MOV A,E

SUB L

MOV L,A

MOV A,D

ADC H

MOV H,A

SHLD 2504

HLT

# EXP 5

8 BIT MULTIPLICATION

MVI D,00

MVI A,00

LXI H,4150

MOV B.,M

INX H

MOV C,M

LOOP: ADD B

JNC NEXT

INR D

NEXT: DCR C

JNZ LOOP

STA 4152

MOV A,D

STA 4153

HLT

EXP 6:

8 BIT DIVISION

LXI H,1100

MOV B,M

MVI C,00

INX H

MOV A,M

LOOP: CMP B

JC SKIP

SUB B

INR C

JMP LOOP

SKIP: STA 1102

MOV A,C

STA 1103

HLT